Zolatron Registers

|  |  |
| --- | --- |
| Last edited: | 14 Sep 2025 |

# IRQ\_REG – $00F6

| 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- |
| ZD\_IRQ | RTC\_ALARM |  |  |  |  |  |  |
| ZolaDOS program on Raspberry Pi has raised an interrupt | RTC alarm triggered an interrupt:  0=No  1=Yes |  |  |  |  |  |  |

# STDIN\_STATUS\_REG – $00F4

| 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- |
| (Reserved) | DUART\_RxB\_BUF\_FULL\_FL | DUART\_RxB\_DAT\_RECVD\_FL | DUART\_RxB\_NUL\_RCVD\_FL | (Reserved) | STDIN\_BUF\_FULL\_FL  DUART\_RxA\_BUF\_FULL\_FL | STDIN\_DAT\_RCVD\_FL  DUART\_RxA\_DAT\_RECVD\_FL | STDIN\_NUL\_RCVD\_FL  DUART\_RxA\_NUL\_RCVD\_FL |
|  | Buffer full  Port B | Data received  Port B | Null received  Port B |  | Buffer full  Port A | Data received  Port A | Null received  Port A |
|  |  |  |  |  |  |  |  |
|  |  |  |  |  |  | Port | B |

We're currently using STDIN\_xx\_FL versions as Port A on the DUART is treated as the STDIN interface.

STDIN\_CLEAR\_FLAGS – This gets ANDed with STDIN\_STATUS\_REG to clear the ***lower*** 4 bits.

# SYS\_REG – $00F5

| 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- |
| ZD\_INT\_FL | (Reserved) | LCD SIZE | (Reserved) | (Reserved) | SYS\_SPI | SYS\_PARALLEL | SYS\_EXMEM |
| ZolaDOS program on Raspberry Pi has raised an interrupt |  | 0=16x2,  1=20x4 |  |  | System is fitted with SPI interface board.  0=No, 1=Yes | System is fitted with parallel interface board.  0=No, 1=Yes | System is fitted with extended memory board.  0=No, 1=Yes |

Constants:

LCD\_TYPE\_16x2 = 0

LCD\_TYPE\_20x4 = 1

# ZD\_CTRL\_REG – PAGE 6

| 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- |
| ZD\_CTRL\_EXCL\_EXT |  |  |  |  |  |  |  |
| Whether to exclude extension from max filename length  \*0 = No 1 = Yes |  |  |  |  |  |  |  |